DISCIPLINE		SEMESTER NAME OF THE TEACHER		
ETC		5TH	5TH NAME OF THE TEACHING FACULTY	
the state of the s		many hands and a second and a second	MISS.SINDHUJA PANIGRA	HI(PTGF)
SUBJECT: Power Electronics & PLC Lab		NO. OF DAYS PER WEEK CLASS ALLOTED : 03 SEMESTER FROM 01/07/2024 T016/12/24		
T	CLASS DAYS			NO. OF WEEKS
WEEKS	1ST		THEORY TOPICS	15 NOS.
1ST WEEK	2ND	Study of V-I char	acteristics of SCR	
	3RD	Study of V-I characteristics of SCR		
2ND WEEK	1ST	Study of V-I char	acteristics of SCR	
	2ND	To study phase c	ontrolled bridge rectifier using resistive load.	
		10 Study phase controlled bridge rectifier using resistive load		
	3RD	To study phase controlled bridge rectifier using resistive load		
3RD WEEK	1ST	To study single-phase Cyclo-converter		
3KD WEEK	2ND	To study single-phase Cyclo-converter		
	3RD	To study single-phase Cyclo-converter		
4TH WEEK	1ST	To perform the speed control of DC motor using Chopper.		
	2ND	To perform the speed control of DC motor using Chopper.		
	3RD	To perform the speed control of DC motor using Chopper.		
	1ST	To study series Inverter.		
5TH WEEK	2ND	To study series Inverter.		
	3RD	To study series Inverter.		
6TH WEEK	1ST	Study of drive circuit for SCR & TRIAC using DIAC.		
	2ND	Study of drive circuit for SCR & TRIAC using DIAC.		
	3RD	Study of drive circuit for SCR & TRIAC using DIAC.		
7TH WEEK	1ST	Introduction/Familiarization PLC Trainer & its Installation with PC		
	2ND	Learn the basics and hardware components of PLC		
	3RD	Understand configuration of PLC system,		
8TH WEEK	1ST	Study various building blocks of PLC		
	2ND	Determine the No. of digital I/O & Analog I/O		
	3RD	Demonstrate PLC and Ladder diagram-Preparation downloading and running		
9TH WEEK	1ST	Execute Ladder diagrams for different Logical Gates		
	2ND	Execute Ladder diagrams for different Logical Gates		
	3RD	Execute Ladder diagrams for different Logical Gates		
10TH WEEK	1ST	Execute Ladder diagrams for different Logical Gates		
	2ND	Execute Ladder diagrams for different Logical Gates		
	3RD	Execute Ladder diagrams for different Logical Gates		
11TH WEEK	1ST	Execute Ladder diagrams using timers		
	2ND	Execute Ladder diagrams using timers		
	3RD	Execute Ladder diagrams using timers		
12TH WEEK	1ST	Execute Ladder diagrams using counters		
	2ND	Execute Ladder diagrams using counters		
	3RD	Execute Ladder diagrams using counters		
	15T	Execute the Ladde	er Diagrams with model applications with DOL	starter
13TH WEEK	2ND	Execute the Ladder Diagrams with model applications with DOL starter		

	3RD	Executo the Ladd
14TH WEEK	1ST	Execute the Ladder Diagrams with model applications with DOL starter Execute Ladder diagrams with model applications with DOL starter
	2ND	Bi dilla Willi Model application of
	3RD	and and with model applications the
15TH WEEK	1ST	adder diagrams with model applications (1) care
	2ND	The state of this strig experiments
	3RD	Repetion of Missing experiments Repetion of Missing experiments

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